

IIIT Sri City M.Tech (Online) Regulations (from Jan. 2026)

1. Preamble

This document outlines the complete regulations for the M.Tech (Online) Program at IIIT Sri City. Currently, there are 3 programs: *Cyber Security*, *IoT & Autonomous Systems* & *VLSI*. This Online M.Tech Programmes is designed to provide learners with a comprehensive understanding of key theoretical concepts and practical applications in the chosen domain. It aims to develop industry-relevant skills through a structured curriculum that includes core courses, electives, projects, and case studies. It is tailor-made for working professionals so that they can get both detailed subject knowledge, theoretical expertise and also exposure to projects. The program emphasizes holistic learning, innovation, and professional growth, enabling participants to engage with real-world problem-solving and emerging technological trends.

Key Highlights of the Programme:

The selection of students for the M.Tech (online) programme is based on the Academic and professional background of the candidate. GATE score is not required. There is no upper limit on the learner's age for getting admission to this programme. This programme is specially designed for the working professionals. Also, the faculty-student live classes and interactive sessions are scheduled during evening hours, weekends, or time slots convenient to learners. .

Programme Delivery: Each M.Tech (online) programme is designed and taught by subject-expert faculty members and researchers from IIIT Sri City, faculty members from other reputed institutes, and leading experts from the respective sectors/industries. Thus, the students can learn from the experts at the forefront of cutting-edge research and technology. This Programme is designed for working professionals. There is no requirement for on-campus staying for the students. All assessment components such as continuous assessments, assignments, and examinations are administered entirely through online mode. Few programmes and project obligations may require short-term stays at the IIIT Sri City campus for laboratory visits, laboratory sessions and demonstrations, as applicable. This is done through a campus immersion program. It also allows becoming an IIT Sri City e-Alumnus with access to all the Alumni privileges and Alumni network.

2. Programmes Offered

- Cyber Security
- IoT & Autonomous Systems
- VLSI

3. Admission Requirements

Eligibility: Explained in detail in below table:

Entry Pathways: GATE + Non-GATE (Working Professionals allowed).

a.	Applicants should have a B.Tech / BE /BS/ M.Tech / MSc (4 semester program) / MCA (minimum 4-semester program) / MS Degree (min. 4 semester) / equivalent degree in the relevant discipline with at least 55% marks or 5.5/10 CPI. In the case of the candidate belonging to SC, ST, or Persons with Disability (PwD) category, this is relaxed to 50% or equivalent 5.0 CGPA/CPI
b.	For MCA/MSC passed graduates, the percentage score of MCA/MSC would be considered. For BE/BTech engineering graduates without PG specialization, the percentage score of the undergraduate degree would be considered.
c.	PG score qualification can be considered.
d.	GATE is not mandatory.

Further, the admission process will be scheduled post-counseling & application process, depending on the number of eligible applications as per seat availability for the program. This entire process will be online. Candidates who do not meet the minimum CGPA or percentage requirement, can still be considered if they provide relevant work experience in the technical field.

Fee Refund Policy

1. Application fees are non-refundable.
2. A 80% refund of the paid course fee is applicable if requested before the batch commencement date.
3. No refund is applicable post batch commencement date.

IIIT Sri City reserves the right to conduct the admissions process. By submitting the application, the students agree that any decision regarding admissions from IIIT Sri City will be final and binding.

4. Programme Duration

The programme total duration is 2 years (4 semesters), delivered fully online.

5. Credit Structure

Total 64 Credits

A. Cyber Security

Semester 1 (16 Credits)

- Introduction to Cyber Security (L: 3, T: 1, P: 0) = 4 Credits
- Number Theory (L: 3, T: 1, P: 0) = 4 Credits
- Operating System and Mobile Security (L: 3, T: 1, P: 0) = 4 Credits
- Penetration Testing (L: 2, T: 0, P: 2) = 4 Credits

Semester 2 (16 Credits)

- Cryptography (L: 3, T: 1, P: 0) = 4 Credits
- Internet Security (L: 3, T: 1, P: 0) = 4 Credits
- Blockchain Technology and Applications (L: 2, T: 0, P: 2) = 4 Credits
- Capstone Project: Securing a Real-World Application (L: 0, T: 0, P: 4) = 4 Credits

Semester 3 (16 Credits)

- Cloud Security (L: 3, T: 1, P: 0) = 4 Credits

- Fusing ML and DL with Cyber Security (L: 2, T: 1, P: 1) = 4 Credits
- System Security (L: 2, T: 0, P: 2) = 4 Credits
- Minor Project: (L: 0, T: 0, P: 4) = 4 Credits

Semester 4 (16 Credits)

- Digital Forensics (L: 2, T:1, P: 1) = 4 Credits
 - Major Project: (L: 0, T: 0, P: 4) = 12 Credits
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B. IoT and Autonomous Systems

Semester 1 (16 Credits)

- Introduction to IoT (L: 3, T: 1, P: 0) = 4 Credits
- Introduction of Cyber Physical System (L: 3, T: 1, P: 0) = 4 Credits
- Python Programming for Networking and Data Science (L: 1, T: 1, P: 2) = 4 Credits
- Wireless Communication (L: 3, T: 1, P: 0) = 4 Credits

Semester 2 (16 Credits)

- Adv. Wireless Communication and IoT Protocols (L: 3, T: 1, P: 0) = 4 Credits
- Intelligent and Autonomous Systems (L: 3, T: 1, P: 0) = 4 Credits
- Technologies for IoT and Autonomous Systems (L: 3, T: 1, P: 0) = 4 Credits
- Capstone Project: End-to-End IoT Application (L: 0, T: 0, P: 4) = 4 Credits

Semester 3 (16 Credits)

- 5G/6G Wireless Communication (L: 3, T: 1, P: 0) = 4 Credits
- Introduction to Cyber Security and Network Security (L: 3, T: 0, P: 1) = 4 Credits
- Digital Twin Concepts and Applications (L: 3, T: 1, P: 0) = 4 Credits
- Minor Project: (L: 0, T: 0, P: 4) = 4 Credits

Semester 4 (16 Credits)

- Applications of Autonomous Systems: Aerial and Underwater Autonomous Vehicle (L: 1, T:1, P: 2) = 4 Credits
 - Major Project: (L: 0, T: 0, P: 4) = 12 Credits
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C. VLSI

Semester 1 (18 Credits)

- Semiconductor Device Physics (L: 3, T: 1, P: 0) = 4 Credits
- VLSI Technology (L: 3, T: 1, P: 0) = 4 Credits
- Digital VLSI Circuit Design (L: 3, T: 1, P: 0) = 4 Credits
- Verilog HDL (L: 3, T: 0, P: 1) = 4 Credits
- Simulation Lab-1 (L: 0, T: 0, P: 2) = 2 Credits

Semester 2 (18 Credits)

- Analog VLSI Design (L: 3, T: 1, P: 0) = 4 Credits
- Low Power VLSI Design (L: 3, T: 1, P: 0) = 4 Credits
- System on Chip (L: 3, T: 1, P: 0) = 4 Credits
- VLSI System Design (L: 3, T: 0, P: 1) = 4 Credits
- Simulation Lab-2 (L: 0, T: 0, P: 2) = 2 Credits

Semester 3 (16 Credits)

- Mixed Signal Circuit Design (L: 3, T: 1, P: 0) = 4 Credits
- Nano Scale Devices (L: 3, T: 1, P: 0) = 4 Credits
- VLSI Testing and Testability (L: 3, T: 1, P: 0) = 4 Credits
- Seminar and Presentation (L: 0, T: 4, P: 0) = 4 Credits

Semester 4 (12 Credits)

- Major Project/Dissertation (L:0, T:0, P:4) = 12 Credit

6. Online Teaching Model

Participants will receive access to live classes, recorded learning materials, the Learning Management System (LMS), virtual and simulation labs, as well as e-content made available by the institute.

7. Evaluation

The evaluation would involve proctored and centralized Mid-exam, End-Sem exam and continuous evaluation component. The continuous evaluation component is between the

faculty/instructor and the students and could involve: Assignments, quizzes, small projects, presentations, etc.

Relative grading will be followed as per the Academic Regulations of IIT Sri City.

This system ensures that students' performance is evaluated in comparison with the overall performance of their cohort. Grade boundaries may be adjusted based on the distribution of marks to maintain fairness, transparency, and academic rigor.

Grade	Points	Description
O	10	Outstanding
A	9	Very good
B	8	Good
C	7	Above Average
D	6	Average
P	5	Pass
F	0	Fail

The MTech (Online) program has typically 11-12 courses. In that, a MTech (Online) student of a particular program can replace 2 courses in the 2nd year (i.e., 3rd/4th Semester) from courses of 1st year (Introductory courses), from other MTech (Online) programs. Further, the student can replace one course in the 2nd/3rd/4th Semester with an NPTEL course. For NPTEL courses, the minimum passing marks is 40/100. The marks to grade conversion is as follows:

≥ 85	O	45 to 54.99	D
75 to 84.99	A	40 to 44.99	P
65 to 74.99	B	< 40	F
55 to 64.99	C		

8. Project / Thesis

Two-phase project with evaluations and thesis submission. Further it depends on the type of course and assessment proposed by the particular course coordinator. Each Team/student could have a separate mentor for their major project.

9. Attendance

Minimum 50% attendance required live online sessions of courses

10. Academic Integrity

Strict anti-plagiarism and exam integrity rules.

11. Graduation Requirements

Completion of 64 credits successfully, including project + Final Senate approval.

12. Re-exam / Supplementary EXAM

All sessions and assessments are scheduled with convenience in mind of working professionals. Requests for Supplementary Exams or date changes or other exceptions will not be accommodated. *A re-exam would be conducted only if a student has genuinely missed either mid-exam or end-exam (only one out of the two), that too with case-by-case recommendation from faculty-in-charge and subsequent approval from IIIT Sri City. The cost for re-exam is Rs. 500 per subject.*

13. Deferral

Once the admission is completed and maximum within 2 weeks of the commencement of the Semester/Program, a student can request for deferral, This request has to be in writing, signed by the candidate; and sent as both soft copy (to mtech.office@iiitsin) and hard copy to the Academic Office, MTech Section, which would be reviewed and approved/rejected by the Institute. The cost for deferral is Rs. 10,000 for the student.

14. Power to Modify

The IIIT Sri City Senate retains the authority to amend the regulations whenever deemed necessary.